

October 30, 2025

**Subject: 18K-2P Inverter Anti-Islanding Method**

To Whom It May Concern:

The purpose of this letter is to declare the anti-islanding methodology used by the following Sol-Ark Inverters, which are listed by TUV Rhineland and tested in accordance with the IEEE 1547.1-2020 and UL1741 3<sup>rd</sup> edition (including supplement SB) product standards:

**Model Name: Limitless 18K-2P-LV**

These inverters use the **Group 1 method of anti-islanding**, based on the definitions provided in the list in PG&E Form No.79-1174-03F, which is replicated below:

- Group 1**     Frequency Shift with continuous positive frequency feedback
- Group 2A**    Frequency Shift with discontinuous or stepped positive frequency feedback
- Group 2B**    Frequency Shift similar to Group 2A except with a dead zone around 60Hz
- Group 2C**    Frequency Shift with unidirectional frequency feedback
- Group 3**     Monitors change of impedance
- Group 4**     Monitors shift at a harmonic frequency (multiple of the fundamental)
- Group 5**     Passive methods like rate of change of frequency, vector shift
- Group 6**     Produces negative sequence current and monitor voltage

If any additional details or clarification on this matter are required, please reach out directly using my contact information below.

Best regards,

*Devin Mangru*

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